



High-end Power Semiconductor Manufacturer

KK1000A 800V-1800V**Fast Switching Thyristor**

- Low switching losses
- Low reverse recovery charge
- Distributed amplified gate for high dI_T/dt



| | | |
|-----------------------------------|-----------|-------------------|
| Mean on-state current | I_{TAV} | 1000 A |
| Repetitive peak off-state voltage | V_{DRM} | 800-1800 V |
| Repetitive peak reverse voltage | V_{RRM} | |
| Turn-off time | t_q | 18.0-50.0 μs |
| $T_j, ^\circ C$ | | -60-125 |

MAXIMUM ALLOWABLE RATINGS

| Symbols and parameters | | Units | Values | Test conditions | |
|------------------------|---|-------------------|--------------|--|---|
| ON-STATE | | | | | |
| I_{TAV} | Mean on-state current | A | 1000 1500 | $T_c= 85^\circ C$; Double side cooled; $T_c= 55^\circ C$; Double side cooled; 180° half-sine wave; 50 Hz | |
| I_{TSM} | Surge on-state current | kA | 14.0 | $T_j=125^\circ C$ | 10ms half sine wave $V_R=0.6V_{RRM}$ |
| I^2t | Safety factor | $A^2s \cdot 10^3$ | 980.0 | $T_j=125^\circ C$ | 10ms half sine wave $V_R=0.6V_{RRM}$ |
| BLOCKING | | | | | |
| V_{DRM}, V_{RRM} | Repetitive peak off-state and Repetitive peak reverse voltages | V | 800-1800 | $T_j=125^\circ C, t_q=10ms$ | |

| SWITCHING | | | | |
|-------------------------------|---|------------|-----------|--|
| (dI_T/dt) _{crit} | Critical rate of rise of on-state current | A/ μ s | 1200 | $V_{DM} = 67\%V_{DRM}$ to 1600A, Gate pulse $t_r \leq 0.5\mu$ s $I_{GM}=1.5A$ |
| THERMAL | | | | |
| T_{stg} | Storage temperature | °C | -40-130 | |
| T_j | Operating junction temperature | °C | -60-125 | |
| MECHANICAL | | | | |
| F | Mounting force | kN | 19.0-26.0 | |

CHARACTERISTICS

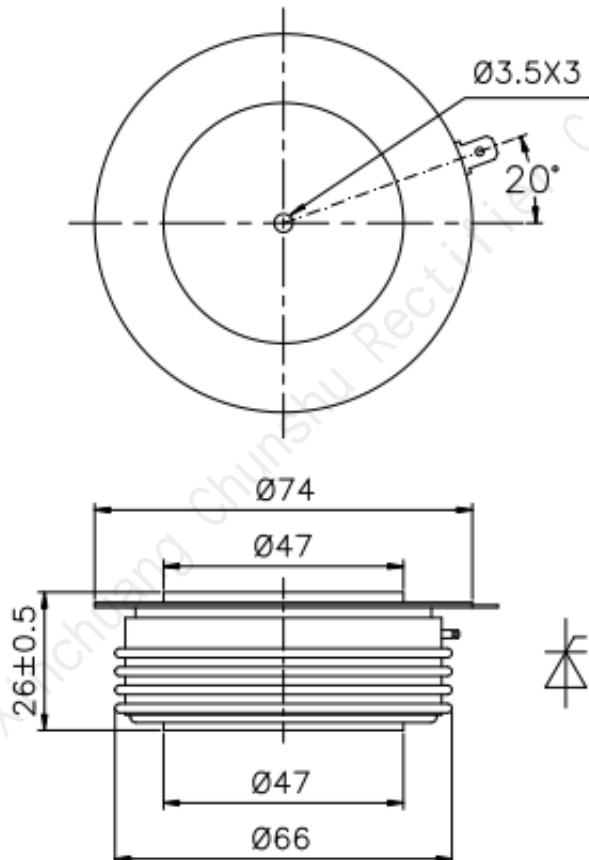
| Symbols and parameters | | Units | Values | Conditions | |
|-------------------------------|---|------------|--------------------|--|--|
| ON-STATE | | | | | |
| V_{TM} | Peak on-state voltage, max | V | 3.15 | $T_j=25$ °C; $I_{TM}=3000A$, F=24.0kN | |
| $V_{T(TO)}$ | On-state threshold voltage, max | V | 1.56 | | |
| r_T | On-state slope resistance, max | $m\Omega$ | 0.33 | $T_j=125$ °C | |
| I_H | Holding current, max | mA | 500 | $V_A=12V$, $I_A=1A$ | |
| BLOCKING | | | | | |
| I_{DRM}, I_{RRM} | Repetitive peak off-state and Repetitive peak reverse currents, max | mA | 80 | $T_j=125$ °C $V_D=V_{DRM}$; $V_R=V_{RRM}$ | |
| (dv_D/dt) _{crit} | Critical rate of rise of off-state voltage ¹⁾ , min | V/ μ s | 1000 | $T_j=125$ °C $V_D=0.67\cdot V_{DRM}$; Gate open | |
| TRIGGERING | | | | | |
| V_{GT} | Gate trigger direct voltage, | V | 0.90Min 3.00Max | $T_j=25$ °C | |
| I_{GT} | Gate trigger direct current, | mA | 40Min 300Max | $T_j=25$ °C | |
| V_{GD} | Gate non-trigger direct voltage, min | V | 0.30 | $T_j=125$ °C ; $V_D=0.67\cdot V_{DRM}$; | |
| SWITCHING | | | | | |
| t_q | Turn-off time ²⁾ , | μ s | 18.0Min | $I_{TM}=1000A, t_p=1000\mu$ s, $V_R=50V$ | |
| | | | 50.0Max | $dv/dt=30V/\mu$ s , $di/dt=-20A/\mu$ s | |
| Q_{rr} | Total recovered charge, max | μ C | 750 | $T_j=125$ °C ; $I_{TM}=1500A, t_p=2000\mu$ s, $di/dt=-60A/\mu$ s, $V_R=50V$ | |

THERMAL

| | | | | |
|------------|--|------|-------|---|
| R_{thjc} | Thermal resistance, junction to case, max | °C/W | 0.020 | At 180°sine, double side cooled Clamping force 7.0kN |
| R_{thch} | Thermal resistance, case to heatsink, max | °C/W | 0.005 | |

MECHANICAL

| | | | | |
|---|-------------|---|-----|--|
| w | Weight, typ | g | 440 | |
|---|-------------|---|-----|--|

OVERALL DIMENSIONS

KT55

All dimensions in millimeters

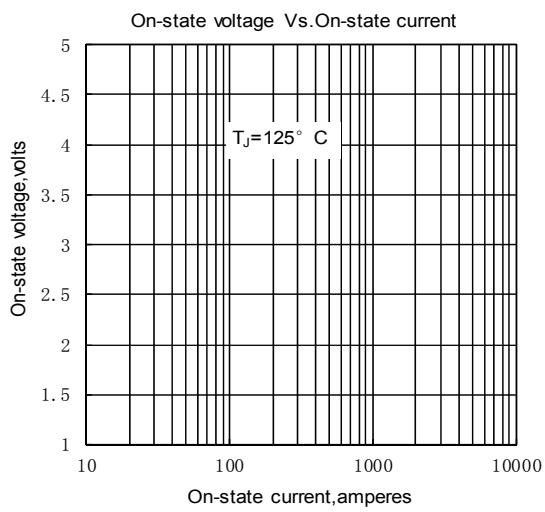


Fig. 1

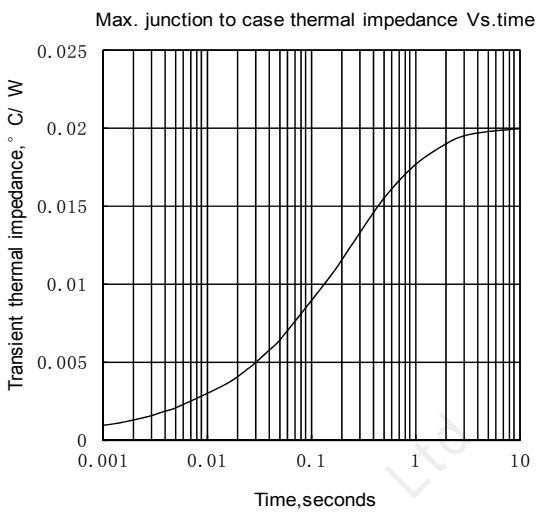


Fig. 2

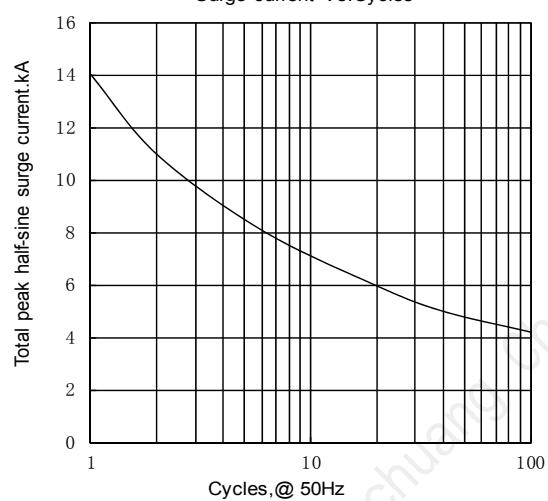


Fig. 3

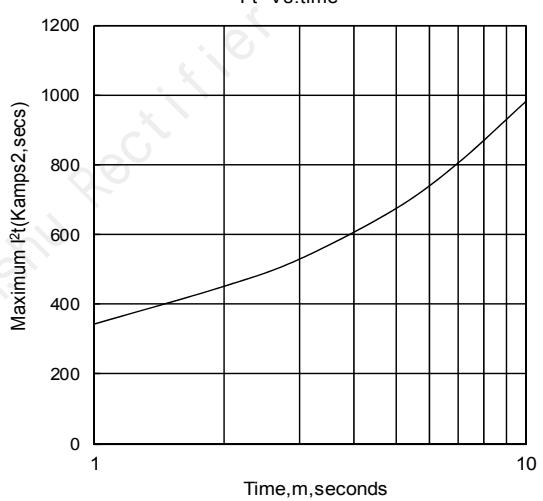


Fig. 4

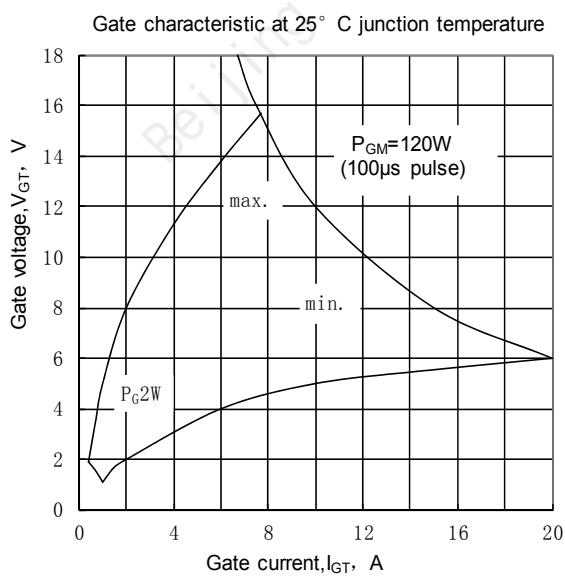


Fig. 5

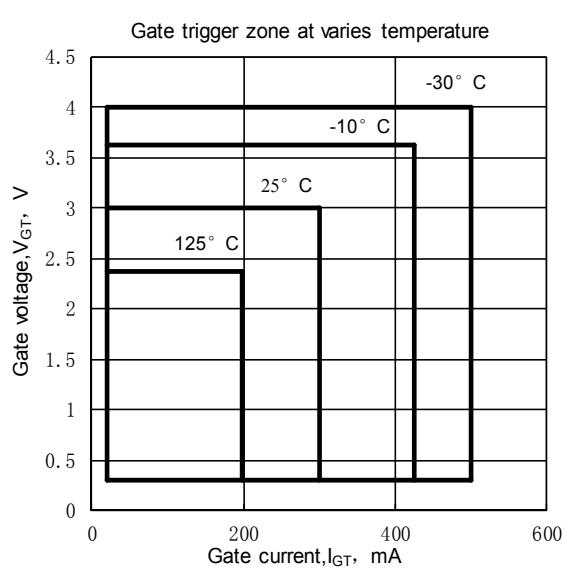


Fig. 6