



高端电力电子器件和装置制造商

## TL193-2500

### 光控晶闸管

<ul style="list-style-type: none"> <li>◆ <math>V_{DRM} = \underline{6200-6400\text{ V}}</math></li> <li>◆ <math>V_{RRM} = \underline{6200-6400\text{ V}}</math></li> <li>◆ <math>I_{T(AV)} = \underline{2005A}</math> (<math>T_C = 85\text{ }^\circ\text{C}</math>)</li> <li>◆ <math>I_{T(AV)} = \underline{2520A}</math> (<math>T_C = 70\text{ }^\circ\text{C}</math>)</li> <li>◆ <math>I_{TSM} = \underline{55\text{ kA}}</math> (<math>T_j = 120\text{ }^\circ\text{C}</math>)</li> <li>◆ <math>P_{LM} = \underline{40\text{ mW}}</math></li> </ul>			
<ul style="list-style-type: none"> <li>◆ 光触发</li> <li>◆ 低通态和开关损耗</li> <li>◆ 叉指型放大门电路</li> </ul>			
最大额定数值			
参数及测试条件	符号	数值	单位
Repetitive peak off-state voltage	$V_{DRM}$	6000, 6200, 6400	V
Repetitive peak reverse voltages	$V_{RRM}$	6000, 6200, 6400	
Repetitive peak off-state current / Repetitive peak reverse current $T_j=120\text{ }^\circ\text{C}$ , $V_D / V_R = V_{DRM} / V_{RRM}$	$I_{DRM} / I_{RRM}$	max.500	mA
Maximum average on-state current $f = 50\text{ Hz}$ , double side cooling $T_C=85\text{ }^\circ\text{C}$ $T_C=70\text{ }^\circ\text{C}$	$I_{T(AV)}$	2005 2520	A
RMS on-state current, $f=50\text{ Hz}$ , $T_C=70\text{ }^\circ\text{C}$	$I_{TRMS}$	3956	
Surge current, $V_R=0$ , $T_j = 120\text{ }^\circ\text{C}$ , $t_p=10\text{ mc}$	$I_{TSM}$	55	kA
Safety current, $T_j=120\text{ }^\circ\text{C}$ , $t_p=10\text{ mc}$	$I^2t$	15125	kA <sup>2</sup> s
Critical rate of rise of on-state current, $V=0.67V_{DRM}$ , $I_T=5000\text{ A}$ , $P_{LM}=40\text{ mW}$ , $t_L=10\text{ }\mu\text{s}$ , $t_{rise}=0.5\text{ }\mu\text{s}$ , $f=50\text{ Hz}$ , $T_j=120\text{ }^\circ\text{C}$	$(di_T/dt)_{crit}$	300	A/ $\mu\text{s}$
Critical rate of rise of off-state voltage, $V_D = 0.67V_{DRM}$ , $T_j = 120\text{ }^\circ\text{C}$	$(dV_D/dt)_{crit}$	1000, 1600, 2000	V/ $\mu\text{s}$
Minimum gate trigger light power, $T_j = 25\text{ }^\circ\text{C}$ , $V_D = 12\text{ V}$	$P_{LM}$	max. 40	mW
Operating temperature	$T_j$	-40... +120	°C
Storage temperature	$T_{stg}$	-40... +50	

电学特性			
Maximum peak on-state voltage, $I_T = 7850 \text{ A}$ , $T_j = 25 \text{ }^\circ\text{C}$	$V_{TM}$	max. 2.75	V
On-state threshold voltage, $T_j = 120 \text{ }^\circ\text{C}$ , $I_T = 4000 - 12000 \text{ A}$	$V_{(TO)}$	max. 1.22	
On-state slope resistance, $T_j = 120 \text{ }^\circ\text{C}$ , $I_T = 4000 - 12000 \text{ A}$	$r_T$	max. 0.28	mΩ
Gate controlled delay time, $V = 1000 \text{ V}$ , $I_T = 2500 \text{ A}$ , $P_{LM} = 40 \text{ mW}$ , $t_L = 10 \text{ } \mu\text{s}$ , $t_{rise} = 0.5 \text{ } \mu\text{s}$ , $T_j = 25 \text{ }^\circ\text{C}$	$t_d$	max. 5.0	μs
Circuit-commutated turn-off time, $I_T = 2500 \text{ A}$ , $di_T/dt = -5 \text{ A}/\mu\text{s}$ , $V_R \geq 100 \text{ V}$ , $V_D = 0.67V_{DRM}$ , ( $dV_D/dt$ ) = $50 \text{ B}/\mu\text{s}$ , $T_j = 120 \text{ }^\circ\text{C}$	$t_q$	typ. 630	
Recovery charge, $di_T/dt = -5 \text{ A}/\mu\text{s}$ , $T_j = 120 \text{ }^\circ\text{C}$ , $I_T = 2500 \text{ A}$ , $V_R \geq 100 \text{ V}$	$Q_{rr}$	max. 6000	μAs
Holding current, $V_D = 12 \text{ V}$ , $T_j = 25 \text{ }^\circ\text{C}$	$I_H$	300	mA
Latching current, $V_D = 12 \text{ V}$ , $T_j = 25 \text{ }^\circ\text{C}$ , $P_{LM} = 40 \text{ mW}$ , $t_L = 10 \text{ } \mu\text{s}$ , $t_{rise} = 0.5 \text{ } \mu\text{s}$	$I_L$	1000	
热学参数			
Thermal resistance junction to case, sin 180°: double side cooled DC: double side cooled	$R_{thjc}$ $R_{thjc}$	0.0067 0.0064	°C/W
Thermal resistance case to heatsink, double side cooled single side cooled	$R_{thch}$	0.0015 0.003	
力学参数			
Weight	w	typ. 3.0	kg
Clamping force	F	70 – 90	kN
Vibration resistance	a	50	m/s <sup>2</sup>
Creepage distance	$D_s$	62	mm
Air strike distance	$D_a$	25.7	mm

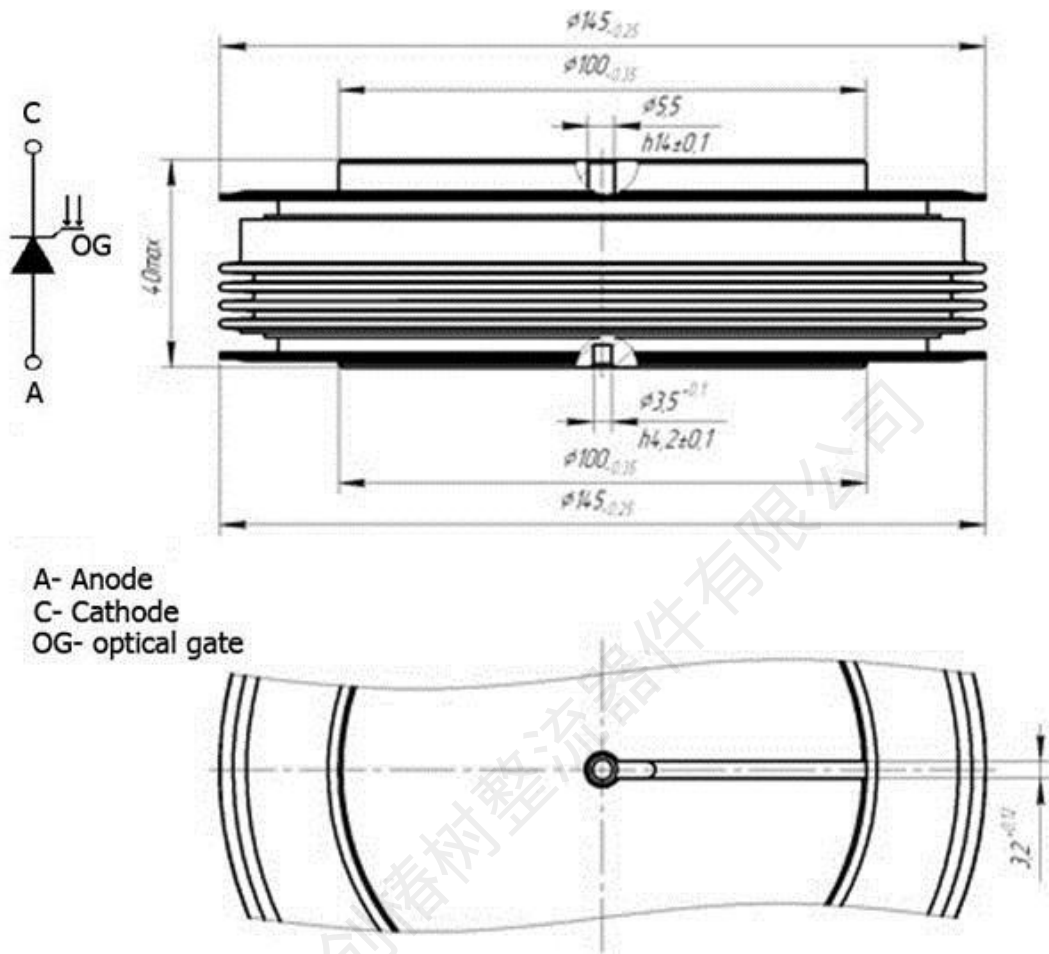


Fig. 1. Device Outline Drawing  
(dimensions in mm)