



High-end Power Semiconductor Manufacturer

KP2810A 6500V

Phase Control Thyristor

- High power cycling capability
- Low on-state and switching losses
- Designed for traction and industrial applications



Mean on-state current	I_{TAV}	2810 A
Repetitive peak off-state voltage	V_{DRM}	6500 V
Repetitive peak reverse voltage	V_{RRM}	
Turn-off time	t_q	800 μ s
$T_{vj}, ^\circ\text{C}$	125	

MAXIMUM ALLOWABLE RATINGS

Symbols and parameters		Units	Values	Test conditions	
ON-STATE					
I _{TAV}	Mean on-state current	A	2810	T _c =70 °C, half-sine wave	
I _{TRMS}	RMS on-state current	A	4410	T _c =70 °C, half-sine wave	
I _{TSM}	Surge on-state current	kA	45.0	t _p = 10 ms	T _{vj} = 125 °C, V _D = V _R = 0 V
			50.0	t _p = 8.3 ms	T _{vj} = 125 °C, V _D = V _R = 0 V
I ² t	Safety factor	A ² s·10 ³	10125	t _p = 10 ms	T _{vj} = 125 °C, V _D = V _R = 0 V
			10375	t _p = 8.3 ms	T _{vj} = 125 °C, V _D = V _R = 0 V
BLOCKING					
V _{DRM} , V _{RRM}	Repetitive peak off-state and Repetitive peak reverse voltages	V	5600	f = 50 Hz, t _p = 10 ms	
V _D , V _R	Direct off-state and Direct reverse voltages	V	0.6·V _{DRM} 0.6·V _{RRM}	T _j =T _{j max} ; Gate open	

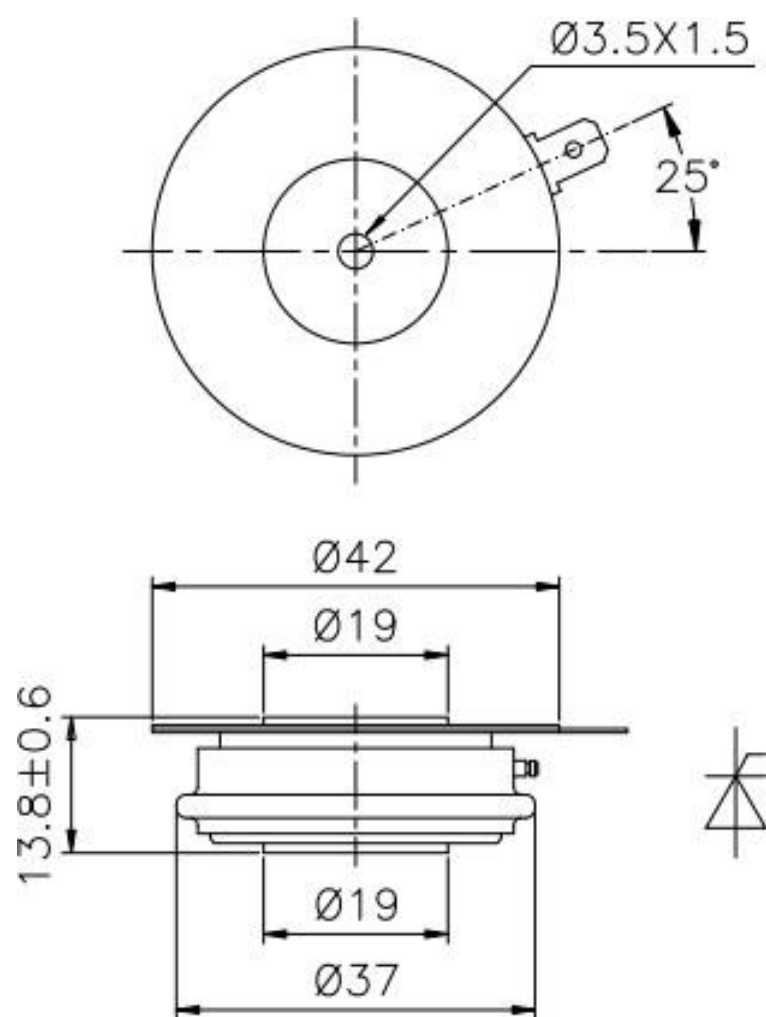
TRIGGERING				
I_{FGM}	Peak forward gate current	A	10	$T_j = T_{j\max}$
V_{RGM}	Peak reverse gate voltage	V	10	
P_G	Gate power dissipation	W	3	
SWITCHING				
$(di_T/dt)_{crit}$	Critical rate of rise of on-state current non-repetitive (f=1 Hz)	A/ μ s	1000	$T_{vj} = 125\text{ }^{\circ}\text{C}$, $I_{TRM} = 3000\text{ A}$, $V_D \leq 0.67 \cdot V_{DRM}$, $I_{FG} = 2\text{ A}$, $t_r = 0.5\text{ }\mu\text{s}$
THERMAL				
T_{stg}	Storage temperature	$^{\circ}\text{C}$	-40-140	
T_{vj}	Operating junction temperature	$^{\circ}\text{C}$	125	
MECHANICAL				
F	Mounting force	kN	81-108	
a	Acceleration	m/s^2	50 100	Device unclamped Device clamped

CHARACTERISTICS

Symbols and parameters		Units	Values	Conditions	
ON-STATE					
V _{TM}	Peak on-state voltage, max	V	2.00	I _T = 3000 A, T _{vj} = 125 °C	
V _{T(TO)}	On-state threshold voltage, max	V	1.12	I _T = 1300 A - 4000 A, T _{vj} = 125 °C	
r _T	On-state slope resistance, max	mΩ	0.29		
I _L	Latching current, max	mA	500	T _{vj} =25°C	
I _H	Holding current, max	mA	125	T _{vj} =25°C	
BLOCKING					
I _{DRM} , I _{RRM}	Repetitive peak off-state and Repetitive peak reverse currents, max	mA	600	V _{DRM} , T _{vj} = 125 °C	
(dv _D /dt) _{crit}	Critical rate of rise of off-state voltage ¹⁾	V/μs	2000	Exp. to 0.67 x V _{DRM} , T _{vj} = 125 °C	
TRIGGERING					
V _{GT}	Gate trigger direct voltage, max	V	2.60	T _{vj} =25 °C	
I _{GT}	Gate trigger direct current, max	mA	400	T _{vj} = 25 °C	
SWITCHING					
t _{gd}	Delay time	μs	3.00	V _D = 0.4.V _{DRM} , I _{FG} = 2 A, t _r = 0.5 μs	
t _q	Turn-off time ²⁾	μs	800	T _{vj} = 125 °C, I _{TRM} = 3000 A, V _R = 200 V, di _T /dt = -1 A/μs, V _D ≤ 0.67.V _{DRM} , dv _D /dt = 20 V/μs	
Q _{rr}	Total recovered charge, max	μAs	3700	T _{vj} = 125 °C, I _{TRM} = 3000 A, V _R = 200 V, di _T /dt = -1 A/μs	

THERMAL					
R _{thjc}	Thermal resistance, junction to case, max	K/kW	5.7	Direct current	Double side cooled
R _{thjc-A}			11.4		Anode side cooled
R _{thjc-K}			11.4		Cathode side cooled
R _{thck}	Thermal resistance, case to heatsink, max	K/kW	1	Double-side cooled	
MECHANICAL					
w	Weight, typ	g	2900		
D _s	Surface creepage distance	mm	56		
D _a	Air strike distance	mm	22		

OVERALL DIMENSIONS



All dimensions in millimeters